

PATENT NUMBER

U.S. UTILITY Patent Application

<p>  O.I.P.E. SCANNED TD4 ca TM </p>	<p> PATENT DATE 1995-01-01 </p>
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APPLICATION NO. 09/808261	CONT/PRIOR	CLASS 438	SUBCLASS 199	ART UNIT 2812	EXAMINER Lattin
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Suraj Mathew
English Teacher

Method to Fabricate Low-Cost High-Performance Transistor CMOS

PTO-2040
12/89

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ (Primary Examiner) (Date)			ISSUE FEE	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____.	_____ (Primary Examiner) (Date)			Amount Due	Date Paid
	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	
<input checked="" type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)				

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